Global Synchronizations for Two-Dimensional Cellular Arrays with Local Communications

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Abstract. The synchronization in cellular automata has been known as a firing squad synchronization problem (FSSP) since its development. The firing squad synchronization problem on cellular automata has been studied extensively for more than fifty years, and a rich variety of synchronization algorithms has been proposed not only for one-dimensional (1D) but two-dimensional (2D) arrays. In the present paper, we focus our attention to the 2D array synchronizers that can synchronize any square/rectangle arrays and construct a survey on recent developments in their designs and implementations of optimum-time and non-optimum-time synchronization algorithms for 2D arrays. The algorithms proposed can be extended to any multi-dimensional arrays without any loss of communication time complexity.

1 Introduction

Synchronization of large scale networks is an important and fundamental computing primitive in parallel and distributed systems. We study a synchronization problem that gives a finite-state protocol for synchronizing cellular automata. The synchronization in cellular automata has been known as a firing squad synchronization problem (FSSP) since its development, in which it was originally proposed by J. Myhill in Moore [1964] to synchronize all parts of self-reproducing cellular automata. The problem has been studied extensively for more than fifty years. See Umeo [2009] for details.

In the present paper, we focus our attention to 2D array synchronizers that can synchronize any square/rectangle arrays and construct a survey on recent developments in their designs and implementations of optimum-time and non-optimum-time synchronization algorithms for 2D arrays. Specifically, we attempt to consider the following questions:

- Is there any new 2D FSSP algorithm other than classical ones?
- What is the smallest implementation of the 2D synchronizer?
- How can we synchronize 2D arrays with the general at any position?
- How do the algorithms compare with each other?
- Can we extend those 2D synchronizers to three-dimensional arrays?

We introduce a rich variety of mapping schemes for the design of 2D array synchronizers. Any configurations of a 1D synchronization algorithm can be mapped efficiently onto a 2D array so that all of the cells on the 2D array would fall into a final synchronization state simultaneously. The mapping schemes we consider include a rotated L-shaped mapping, a zebra mapping, a diagonal mapping, and a one-sided recursive-halving marking based mapping.

2 Firing Squad Synchronization Problem and Algorithms

Consider a finite 2D rectangular array consisting of $m \times n$ cells arranged in m rows and n columns. Each cell is an identical (except the border cells) finite-state automaton. The array operates in lock-step mode in such a way that the next state of each cell (except border cells) is determined by both its own present state and the present states of its north, south, east and west neighbors. Thus, we assume the von Neumann-type four nearest neighbors. The firing squad synchronization problem is to determine a finite-state local description (state set and next-state function) for cells that ensures all cells enter the *fire* state at exactly the same time and for the first time, thus realizing a global synchronization.

Implementations	# of states	# of rules	complexity	model	Mapping
Beyer [1969] Algorithm A ₁		_	2n - 2	O(1)-bit	L-shaped
Shinahr [1974] Algorithm A ₁	17	_	2n - 2	O(1)-bit	L-shaped
Umeo, Maeda and Fujiwara [2002] Algorithm A ₁	9	1718	2n - 2	O(1)-bit	L-shaped
Umeo and Kubo [2010] Algorithm A ₂	7	787	2n - 2	O(1)-bit	Zebra
Umeo, Maeda, Hisaoka, and Teraoka [2006] Algorithm A ₃	6	942	4n - 4	O(1)-bit	Diagonal
Ishii et al. [2006] Algorithm A ₃	15	1614	2n - 2	O(1)-bit	Diagonal
Umeo, Uchino and Nomura [2011] Algorithm A ₄	37	3271	2n - 2	O(1)-bit	Recursive- Halving
Gruska, Torre and Parente [2007] Algorithm A ₁			2n - 2	1-bit	L-shaped
Umeo and Yanagihara [2011] Algorithm A ₁	49	237	2n - 2	1-bit	L-shaped

 Table 1. A list of FSSP algorithms for square arrays.

In Tables 1 and 2 we present a list of implementations of square/rectangle FSSP algorithms for cellular automata with O(1)-bit or 1-bit communications. The O(1)-bit communication model is a usual cellular automaton in which the amount of communication bits exchanged in one step between neighboring cells is assumed to be O(1) bits. The 1-bit communication model is a subclass of the O(1)-bit model, in which inter-cell communication is restricted to 1-bit communication.

 Table 2. A list of FSSP algorithms for rectangle arrays.

Algorithms & Implementations	# of states	# of rules	Time complexity	Communication model	Mapping
Beyer [1969] Algorithm A ₁	_		m + n + max(m, n) - 3	O(1)-bit	L-shaped
Shinahr [1974] Umeo et al. [2009] Algorithm A ₁	28 28	12849*	m + n + max(m, n) - 3	O(1)-bit	L-shaped
Umeo and Nomura [2010] Algorithm A ₂	10	1629	m + n + max(m, n) - 2	O(1)-bit	Zebra
Umeo, Hisaoka and Akiguchi [2005] Algorithm A ₃	12	1532	m + n + max(m, n) - 3	O(1)-bit	Diagonal
Umeo, Maeda, Hisaoka, and Teraoka [2006] Algorithm A ₃	6	942	2m + 2n - 4	O(1)-bit	Diagonal
Umeo, Nishide and Yamawaki [2011] Algorithm A ₄	384	112690	m + n + max(m, n) - 3	O(1)-bit	Recursive- Halving

The readers can see how a rich variety of 2D FSSP algorithms exists. Some algorithms can be easily extended to 3D arrays. The embedding schemes developed in this paper would be useful for further implementations of multi-dimensional synchronization algorithms.

References

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